

In re Application of:
Robert Marc Zeidman

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For: System and Method for
Connecting a Logic Circuit
Simulation to a Network

Group Art Unit: 2128
Examiner: Luu, Cuong V.
Conf. No.: 9153

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****CERTIFICATE OF E-FILED TRANSMISSION****
I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below

B. Noël Kivlin
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B June 15, 2009
Signature Date

RESPONSE TO FINAL OFFICE ACTION
MAILED APRIL 16, 2009

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper is submitted in response to the Final Office Action of April 16, 2009.